Features:

- User Programmable Hysteresis and Temperature Set Point
- Easily Programs with 2 External Resistors
- Wide Temperature Detection Range:
  - -0°C to 70°C: (TC620/TC621CCX)
  - -40°C to +125°C: (TC620/TC621CVX)
  - -40°C to +85°C: (TC620/TC621CEX)
  - -55°C to +125°C: (TC620/TC621CMX)
- Onboard Temperature Sensing Applications (TC620X)
- External NTC Thermistor for Remote Sensing Applications (TC621X)
- Available in 8-Pin PDIP and SOIC Packages

Applications:

- Power Supply Over Temperature Detection
- Consumer Equipment
- Temperature Regulators
- CPU Thermal Protection

Device Selection Table

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
<th>Temperature Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>TC620X*COA</td>
<td>8-Pin SOIC</td>
<td>0°C to +70°C</td>
</tr>
<tr>
<td>TC620X*CPA</td>
<td>8-Pin PDIP</td>
<td>0°C to +70°C</td>
</tr>
<tr>
<td>TC620X*EPA</td>
<td>8-Pin SOIC</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>TC620X*EOA</td>
<td>8-Pin PDIP</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>TC620X*VOA</td>
<td>8-Pin SOIC</td>
<td>-40°C to +125°C</td>
</tr>
<tr>
<td>TC621X*COA</td>
<td>8-Pin SOIC</td>
<td>0°C to +70°C</td>
</tr>
<tr>
<td>TC621X*CPA</td>
<td>8-Pin PDIP</td>
<td>0°C to +70°C</td>
</tr>
<tr>
<td>TC621X*EPA</td>
<td>8-Pin SOIC</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>TC621X*EPA</td>
<td>8-Pin PDIP</td>
<td>-40°C to +85°C</td>
</tr>
</tbody>
</table>

Note: *The part code will be C or H (see Functional Block Diagrams).

Package Type

General Description:

The TC620 and TC621 are programmable logic output temperature detectors designed for use in thermal management applications. The TC620 features an onboard temperature sensor, while the TC621 connects to an external NTC thermistor for remote sensing applications.

Both devices feature dual thermal interrupt outputs (HIGH LIMIT and LOW LIMIT), each of which is programmed with a single external resistor. On the TC620, these outputs are driven active (high) when measured temperature equals the user programmed limits. The CONTROL (hysteresis) output is driven high when temperature equals the high limit setting and returns low when temperature falls below the low limit setting. This output can be used to provide ON/OFF control to a cooling fan or heater. The TC621 provides the same output functions except that the logical states are inverted.

The TC620/TC621 are usable over operating temperature ranges of 0°C to 70°C, -40°C to +125°C and -55°C to 125°C.
*Suffix code "C" denotes cooling option (High true CONTROL output).
Suffix code "H" denotes heating option (Low true CONTROL output).
1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings*

Supply Voltage ....................................................... 20V
Input Voltage Any Input.. (GND – 0.3V) to (VDD +0.3V)
Package Power Dissipation (TA ≤ 70°C)
  PDIP ............................................. 730 mW
  SOIC............................................. 470 mW
Derating Factors:
  Plastic .......................................... 8 mW/°C
Operating Temperature:
  M Version ......................... -55°C to +125°C
  V Version ......................... -40°C to +125°C
  E Version ......................... -40°C to +85°C
  C Version ......................... 0°C to +70°C
Storage Temperature ......................... -65°C to +150°C

*Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

TC620/TC621 ELECTRICAL SPECIFICATIONS

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
<th>Test Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>VDD</td>
<td>Supply Voltage Range</td>
<td>4.5</td>
<td>—</td>
<td>18</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>IDD</td>
<td>Supply Current</td>
<td>—</td>
<td>270</td>
<td>400</td>
<td>μA</td>
<td>5V ≤ VDD ≤ 18V</td>
</tr>
<tr>
<td>ROUT</td>
<td>Output Resistance</td>
<td>—</td>
<td>400</td>
<td>1000</td>
<td>Ω</td>
<td>Output High or Low, 5V ≤ VDD ≤ 18V</td>
</tr>
<tr>
<td>IOUT</td>
<td>Output Current</td>
<td>—</td>
<td>—</td>
<td>1</td>
<td>mA</td>
<td>Temp. Sensed Source/Sink</td>
</tr>
<tr>
<td>IOUT</td>
<td>Output Current</td>
<td>—</td>
<td>—</td>
<td>1</td>
<td>mA</td>
<td>Cool/Heat Source/Sink</td>
</tr>
<tr>
<td>TERR</td>
<td>Absolute Accuracy</td>
<td>T - 3</td>
<td>T</td>
<td>T + 3</td>
<td>°C</td>
<td>T = Programmed Temperature</td>
</tr>
</tbody>
</table>
2.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 2-1.

**TABLE 2-1: PIN FUNCTION TABLE**

<table>
<thead>
<tr>
<th>Pin No. (8-Pin PDIP) (8-Pin SOIC)</th>
<th>Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0°C to +70°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>-40°C to +85°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>-40°C to +125°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>NC</td>
<td>No Internal Connection.</td>
</tr>
<tr>
<td>2</td>
<td>LOW SET</td>
<td>Low temperature set point. Connect an external 1% resistor from LOW SET to $V_{DD}$ to set trip point.</td>
</tr>
<tr>
<td>3</td>
<td>HIGH SET</td>
<td>High temperature set point. Connect an external 1% resistor from HIGH SET to $V_{DD}$ to set trip point.</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
<td>Ground Terminal.</td>
</tr>
<tr>
<td>5</td>
<td>CONTROL</td>
<td>Control output.</td>
</tr>
<tr>
<td>6</td>
<td>HIGH LIMIT</td>
<td>High temperature push/pull output.</td>
</tr>
<tr>
<td>7</td>
<td>LOW LIMIT</td>
<td>Low temperature push/pull output.</td>
</tr>
<tr>
<td>8</td>
<td>$V_{DD}$</td>
<td>Power supply input.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Pin No. (8-Pin PDIP) (8-Pin SOIC)</th>
<th>Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0°C to +70°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>-40°C to +85°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>THERMISTOR</td>
<td>Thermistor input.</td>
</tr>
<tr>
<td>2</td>
<td>HIGH SET</td>
<td>High temperature set point. Connect an external 1% resistor from HIGH SET to $V_{DD}$ to set trip point.</td>
</tr>
<tr>
<td>3</td>
<td>LOW SET</td>
<td>Low temperature set point. Connect an external 1% resistor from LOW SET to $V_{DD}$ to set trip point.</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
<td>Ground Terminal.</td>
</tr>
<tr>
<td>5</td>
<td>CONTROL</td>
<td>Control output.</td>
</tr>
<tr>
<td>6</td>
<td>LOW LIMIT</td>
<td>Low temperature push/pull output.</td>
</tr>
<tr>
<td>7</td>
<td>HIGH LIMIT</td>
<td>High temperature push/pull output.</td>
</tr>
<tr>
<td>8</td>
<td>$V_{DD}$</td>
<td>Power supply input.</td>
</tr>
</tbody>
</table>
3.0 DETAILED DESCRIPTION

The TC620 has a positive temperature coefficient temperature sensor and a dual threshold detector. Temperature set point programming is accomplished with external resistors from the HIGH SET and LOW SET inputs to $V_{DD}$. The HIGH LIMIT and LOW LIMIT outputs remain low as long as measured temperature is below set point values. As measured temperature increases, the LOW LIMIT output is driven high when temperature equals the LOW SET set point ($\pm 3^\circ C$ max). If temperature continues to climb, the HIGH LIMIT output is driven high when temperature equals the HIGH SET set point (Figure 3-1). The CONTROL (hysteresis) output is latched in its active state at the temperature specified by the HIGH SET resistor. CONTROL is maintained active until temperature falls to the value specified by the LOW SET resistor.

Care must also be taken to ensure the LOW SET temperature setting is at least $5^\circ C$ lower than the HIGH SET temperature setting.

Figure 3-2 can help the user obtain an estimate of the external resistor values required for the desired LOW SET and HIGH SET trip points.

3.1 Programming the TC620

The resistor values to achieve the desired trip point temperatures on HIGH SET and LOW SET are calculated using Equation 3-1:

$$R_{TRIP} = 0.5997 \times T^{2.1312}$$

Where:

- $R_{TRIP}$ = Programming resistor in Ohms
- $T$ = The desired trip point temperature in degrees Kelvin.

For example, a $50^\circ C$ setting on either the HIGH SET or LOW SET input is calculated using Equation 3-2 as follows:

$$R_{SET} = 0.5997 \times ((50 + 273.15)^{2.1312}) = 133.6 \, k\Omega$$

Care must be taken to ensure the LOW SET programming resistor is a smaller value than the HIGH SET programming resistor. Failure to do this will result in erroneous operation of the CONTROL output.

3.2 Built-in Hysteresis

To prevent output “chattering” when measured temperature is at (or near) the programmed trip point values, the LOW SET and HIGH SET inputs each have built-in hysteresis of $-2^\circ C$ below the programmed settings (Figure 3-3).

As shown, the outputs remain in their active state (hysteresis) until temperature falls an additional $2^\circ C$ below the user’s setting.
3.3 Using the TC621

The TC621 operation is similar to that of the TC620, but requires an external NTC thermistor. Use the resistance versus temperature curve of the thermistor to determine the values of the programming resistors. Note that the pin numbers for the HIGH SET and LOW SET programming resistors for the TC621 are reversed versus that of the TC620 (i.e., the resistor value on HIGH SET [Pin 2] should always be lower than the one connected to LOW SET [Pin 3]). Also note that the outputs of the TC621 are LOW TRUE when used with an NTC thermistor.

3.4 TC621 Thermistor Selection

The TC621 uses an external thermistor to monitor the controlling temperature. A thermistor with a resistance value of approximately 100 kΩ at 25°C is recommended.

A temperature set point is selected by picking a resistor whose value is equal to the resistance of the thermistor at the desired temperature. For example, using the data shown in Figure 3-4, a 30 kΩ resistor between HIGH TEMP (Pin 2) and VDD (Pin 8) sets the high temperature trip point at +51°C and a 49 kΩ resistor on LOW TEMP (Pin 3) sets the low temperature trip point to +41°C.

3.5 TC620/TC621 Outputs

Both devices have complimentary output stages. They are rated at a source or sink current of 1 mA maximum.
4.0 TYPICAL APPLICATIONS

4.1 Dual Speed Temperature Control

In Figure 4-1, the Dual Speed Temperature Control uses a TC620 and a TC4469 quad driver. Two of the drivers of the TC4469 are configured in a simple oscillator. When the temperature is below the LOW TEMP set point, the output of the driver is OFF. When the temperature exceeds the LOW TEMP set point, the TC4469 gates the oscillator signal to the outputs of the driver. This square wave signal modulates the remaining outputs and drives the motor at a low speed. If this speed cannot keep the temperature below the HIGH TEMP set point, then the driver turns on continuously which increases the fan speed to high. The TC620 will monitor the temperature and only allow the fan to operate when needed and at the required speed to maintain the desired temperature. A higher power option can be designed by adding a resistor and a power MOSFET.

FIGURE 4-1: Dual Speed Temperature Control

4.2 Temperature Controlled Fan

In the application in Figure 4-2, a high and a low temperature is selected by two $R_L$ and $R_H$. The TC620 monitors the ambient temperature and turns the FET switch on when the temperature exceeds the HIGH TEMP set point. The fan remains on until the temperature decreases to the LOW TEMP set point. This provides the hysteresis. In this application, the fan turns on only when required.

The TC621 uses an external thermistor to monitor the ambient temperature. This adds one part, but allows more flexibility with the location of the temperature sensor.

FIGURE 4-2: Temperature Controlled Fan
FIGURE 4-3: Heating and Cooling Application
5.0 PACKAGING INFORMATION

5.1 Package Marking Information
Package marking data not available at this time.

5.2 Taping Form

Component Taping Orientation for 8-Pin SOIC (Narrow) Devices

User Direction of Feed

Standard Reel Component Orientation for 713 Suffix Device

<table>
<thead>
<tr>
<th>Carrier Tape, Number of Components Per Reel and Reel Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>Package</td>
</tr>
<tr>
<td>----------------</td>
</tr>
<tr>
<td>8-Pin SOIC (N)</td>
</tr>
</tbody>
</table>

5.3 Package Dimensions

8-Pin Plastic DIP

Dimensions: inches (mm)
8-Pin SOIC

Dimensions: inches (mm)
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Data Sheets
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