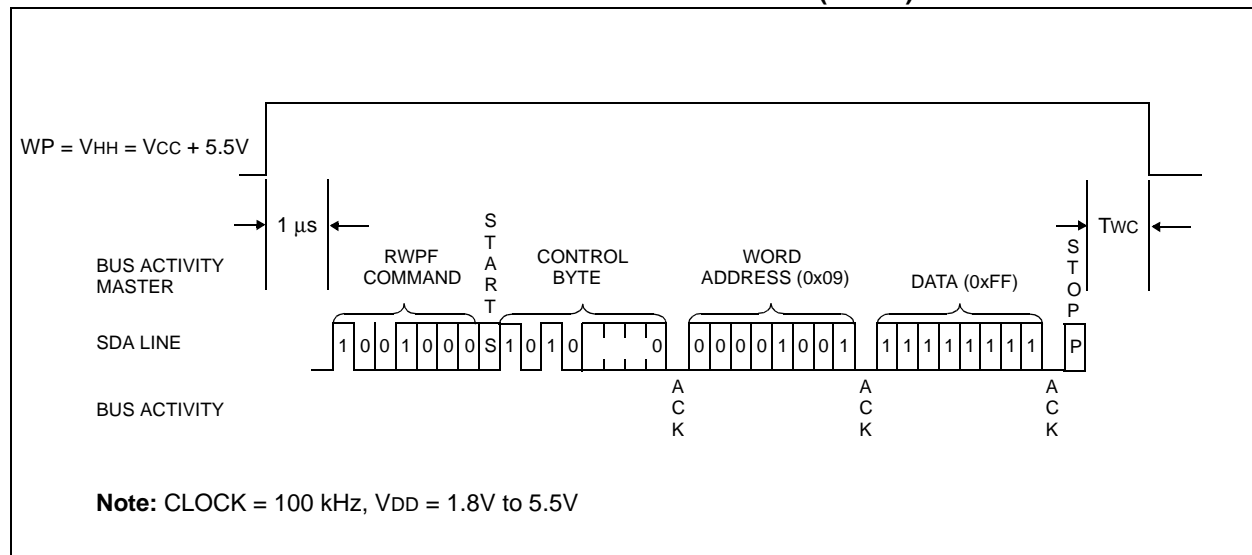


Clearing the Software Write-Protect Feature on the 24LCS52

INTRODUCTION

The purpose of this document is to show customers how to clear the software write-protect feature on the 24LCS52. The full command will be shown in Figure 1. The customer should allow for 1 μ s delay after V_{HH} is applied to WP and 5 ms delay after the Stop bit for T_{WC}.

FIGURE 1: COMMAND TO RESET WRITE-PROTECT FUSE (RWPF)



TB075

NOTES:

Note the following details of the code protection feature on Microchip devices:

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