
Section 51. Introduction (Part VI)

HIGHLIGHTS

This section of the manual contains the following major topics:

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Note: This family reference manual section is meant to serve as a complement to device data sheets. Depending on the device variant, this manual section may not apply to all dsPIC33F/PIC24H devices.

Please consult the note at the beginning of the “**Device Overview**” chapter in the current device data sheet to check whether this document supports the device you are using.

Device data sheets and family reference manual sections are available for download from the Microchip Worldwide Web site at: <http://www.microchip.com>

51.1 INTRODUCTION

Part VI sections of the “*dsPIC33F/PIC24H Family Reference Manual*” provide information on modules specific to the ultra low cost, small pin count Digital Signal Controllers (DSCs). These devices are ideally suited for low cost, high-performance motor control applications in addition to a variety of sensor applications.

These devices feature many advanced peripherals, such as Motor Control PWM, 10-bit Analog-to-Digital Converter (ADC), Comparator with Blanking, and Charge Time Measurement Unit (CTMU). In addition, these devices also feature Peripheral Pin Select (PPS) functionality, which provides flexibility of mapping any digital peripheral to any desired digital pin.

Part VI sections apply to the following dsPIC33F devices:

- dsPIC33FJ16MC101
- dsPIC33FJ16MC102
- dsPIC33FJ16GP101
- dsPIC33FJ16GP102

[Table 51-1](#) provides a guide to specific information about these devices.

Table 51-1: Family Reference Manual Information Guide

For Information About:	See This Section:
Device Overview	Part I, Section 1
CPU	Part I, Section 2
Data Memory	Part I, Section 3
Program Memory	Part I, Section 4
Flash Programming	Part I, Section 5
Reset	Part I, Section 8
Watchdog Timer (WDT) and Power-Saving Modes	Part I, Section 9
Timers	Part I, Section 11
Input Capture	Part I, Section 12
Output Compare	Part I, Section 13
Motor Control PWM	Part I, Section 14
Analog-to-Digital Converter (ADC)	Part I, Section 16
UART	Part I, Section 17
Serial Peripheral Interface (SPI)	Part I, Section 18
Inter-Integrated Circuit™ (I ² C™)	Part I, Section 19
CodeGuard™ Security	Part I, Section 23
Programming and Diagnostics	Part I, Section 24
Device Configuration	Part I, Section 25
Development Tool Support	Part I, Section 26
I/O Ports with Peripheral Pin Select (PPS)	Part II, Section 30
Real-Time Clock and Calendar (RTCC)	Part III, Section 37
Oscillator (Part VI)	Part VI, Section 52
Interrupts (Part VI)	Part VI, Section 53
Comparator with Blanking	Part VI, Section 54
Charge Time Measurement Unit (CTMU)	Part VI, Section 55

51.2 REVISION HISTORY

Revision A (January 2011)

This is the initial released version of this document.

Note the following details of the code protection feature on Microchip devices:

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