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## Section 40. Introduction (Part IV)

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### HIGHLIGHTS

This section of the manual contains the following major topics:

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## 40.1 INTRODUCTION

Section Part IV of the dsPIC33F Family Reference Manual provides information on modules specific to the dsPIC33F Switch Mode Power Supply (SMPS) digital signal controllers. These devices include peripherals that are ideally suited for power control applications.

dsPIC33F SMPS devices feature several advanced peripherals, such as a high resolution PWM, a High-Speed (up to 4 Msp/s) 10-bit Analog-to-Digital Converter (ADC) and a High-Speed Analog Comparator module. In addition, these devices feature Peripheral Pins Select functionality, which provides flexibility of mapping any digital peripheral functionality to any desired digital pin.

Part IV sections apply to the following dsPIC33F devices:

- dsPIC33FJ06GS101
- dsPIC33FJ06GS102
- dsPIC33FJ06GS202
- dsPIC33FJ16GS402
- dsPIC33FJ16GS404
- dsPIC33FJ16GS502
- dsPIC33FJ16GS504

Table 40-1 provides a guide to specific information about these devices.

**Table 40-1: Family Reference Manual Information Guide**

For Information About:	See This Section:
CPU	Part I, Section 2
Data Memory	Part I, Section 3
Program Memory	Part I, Section 4
Flash Programming	Part I, Section 5
Reset	Part I, Section 8
Watchdog Timer and Power-Saving Modes	Part I, Section 9
Timers	Part I, Section 11
Input Capture	Part I, Section 12
Output Compare	Part I, Section 13
Universal Asynchronous Receiver Transmitter (UART)	Part I, Section 17
Serial Peripheral Interface (SPI™)	Part I, Section 18
Inter-Integrated Circuit (I <sup>2</sup> C™)	Part I, Section 19
CodeGuard™ Security	Part I, Section 23
Programming and Diagnostics	Part I, Section 24
Device Configuration	Part I, Section 25
Development Tool Support	Part I, Section 26
I/O Ports with Peripheral Pin Select	Part II, Section 30
Interrupts	Part IV, Section 41
Oscillator	Part IV, Section 42
High-Speed Power Supply PWM	Part IV, Section 43
High-Speed 10-bit Analog-to-Digital Converter (ADC)	Part IV, Section 44
High-Speed Analog Comparator	Part IV, Section 45

### 40.2 REVISION HISTORY

#### Revision A (November 2007)

This is the initial released revision of this document.

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