
Section 27. Introduction (Part II)

HIGHLIGHTS

This section of the manual contains the following major topics:

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27.1 INTRODUCTION

Part II sections of the PIC24H Family Reference Manual provide information on modules specific to the low-cost, small pin count digital signal controllers (DSCs). These devices are ideally suited for low-cost, high-performance sensor applications.

These devices feature many advanced peripherals, such as an Analog-to-Digital Converter, as well as Peripheral Pins Select functionality, which provides flexibility of mapping any digital peripheral functionality to any desired digital pin.

Part II sections apply to the following devices:

- PIC24HJ12GP201
- PIC24HJ12GP202
- PIC24HJ16GP304
- PIC24HJ32GP202
- PIC24HJ32GP204

Table 27-1 provides a guide to specific information about these devices:

Table 27-1: Information Guide

For Information About:	See This Section:
CPU	Part I, Section 2
Data Memory	Part I, Section 3
Program Memory	Part I, Section 4
Flash Programming	Part I, Section 5
Interrupts	Part II, Section 29
Oscillator	Part I, Section 7
Reset	Part I, Section 8
WatchDog Timer and Power Savings Modes	Part I, Section 9
I/O Ports with Peripheral Pin Select	Part II, Section 30
Timers	Part I, Section 11
Input Capture	Part I, Section 12
Output Compare	Part I, Section 13
10-/12-bit ADC Without DMA	Part II, Section 28
UART	Part I, Section 17
SPI™	Part I, Section 18
I ² C™	Part I, Section 19
CodeGuard™ Security	Part I, Section 23
Programming and Diagnostics	Part I, Section 24
Device Configuration	Part I, Section 25
Development Tool Support	Part I, Section 26

27.2 REVISION HISTORY

Revision A (May 2007)

This is the initial released version of this document.

NOTES: