

Home Security System

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APPLICATION OPERATION:

The small size, and low cost of PIC12C508 controllers make them very suitable for use as the heart of home/ small business security systems. This design demonstrates one such system that can easily be made. Additional functionality (monitored loops) and/or redundancy can be obtained if desired, by duplicating this circuit and possibly combining the outputs.

Inputs from a normally closed 'delayed' loop, from a normally closed 'instantaneous' loop, and from a normally open 'panic button' circuit are all monitored by the PIC12C508, and used to drive two alarm outputs.

The delayed loop is configured to provide a 45 second delay on alarm in order to allow the user sufficient time to enter or exit the monitored zone. Typically, this loop would be used for magnetic door switches or for infrared body sensors. During this 45 second period, the alarm LED pulses on and off rapidly to warn of impending activation.

The instantaneous loop provides no appreciable delay (other than a 0.5 sec debounce period) before causing the alarm to sound, unless the system has just been armed and 45 seconds has not yet elapsed. During this period, the loop acts like the delayed loop allowing the user time to check the loop monitoring LEDs and exit. This loop would typically be used to monitor other points such as windows.

The final type of trigger is from a panic button circuit. As many switches as are required can be wired in parallel, and placed in convenient locations for manually triggering the alarm, at any time. All three triggers cause the alarm outputs to go to active levels. On alarm, the pulsed output (pin 2) will alternate between +5V, for about1.5 sec, and 0V for 0.5 sec, while the constant output (pin 3) will simply go to +5V. The pulsed output can be used to drive a siren circuit, while the constant output could be useful for turning lights on, etc. Both will remain in this alarm state until reset by the removal of power (typically through a key switch) or for 5 minutes, whichever occurs first.

There is a possibility that an alarm may have occurred and been automatically reset by the system without the knowledge of the user. In order to signal this condition, during the alarm period and afterwards until the system is reset, the LED output (pin 5) will be driven alternately low for 1.5 sec, and high for 0.5 sec, mimicking the pulsed alarm output. This pulsing of the LED output will have no effect on the normal operation of the system, though.

This application uses the WatchDog Timer for automatic reset on software errors, and the internal oscillator for system timing. Power is connected +5V (Vdd) to pin 1 and 0V (Vss) to pin 8. The normally closed delayed loop is connected between Vss and pin 7, while the normally closed instantaneous loop is wired between Vss and pin 6. The panic switch circuit is connected between pin 4 and Vss.

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Graphical hardware representation:



APPENDIX A: SOURCE CODE

;									
;					Home Alarm				
;									
;						by Jim Nagy.	November 1997		
;						by offic hagy,			
;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;	A program to use the PIC12C508 as a security alarm system controller. Provides three different monitored inputs (two normally closed, and one normally open) and two outputs (one pulsed, one steady). A LED drive output is provided as well for driving a warning/alarm LED. Pin connections are as follows:								
;	Pin	1:	Vdd (+!	5V)					
, ; ;	Pin	2:	Active when in	high pulsed alarr n alarm). Resets a	n output (+5V for after 5 mins.	1.5sec and OV	for 0.5 sec		
;	Pin	3:	Active	high constant ala	arm output (+5V).	Resets after 5	5 mins.		
; ; ; ;	Pin	4:	Normal between alarm f	ly open input. Typ n this pin and Vss to occur. Alarm re	pically used for p s. Momentary conne esets after 5 minu	anic switches ction to Vss v tes, if the ci	connected will cause an ircuit is open.		
;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;	Pin	5:	Active is in t in prog	low LED drive out the 45sec. delayed gress/has occurred	put. Pulses rapid alarm period, or d.	ly to warn tha pulses slowly	at the system / if an alarm is		
; ; ; ; ;	Pin 6: Normally closed loop input. Has 0.5 sec debounce period on change of input to reduce the possibility of nuisance triggers. Initiates an alarm immediately after the loop is found to be open, but will not retrigger should the loop remain open. Not enabled during the first 45 seconds after the alarm is first turned on. Typically used for window switches.								
; ; ;	Pin 7: Similar to pin 6, but the alarm is delayed by 45 seconds, to allow time for entry/exit. Typically used for door switches.								
;;	Pin	8:	Vss (07	V)					
;****	* * * *	* * * *	* * * * * * * * *	* * * * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * *	* * *			
, Prog	gram ⊺n	вqu	ales Fou 0	: CDI	0 nin names				
Inst Ti	 1		EQU U EQU 1	/ 011	o pin names				
LED	-		EOU 2						
Panic	In		EOU 3						
Const			EOU 4						
Pulse			EQU 5						
DBPani	ic		EOU 12	; par	ic button debounce	e for 12 *2.04	8mS		
DBDela	ay		EQU 244	; del	ay loop -> 500mS d	debounce			
DBInst	t		EQU 244	; ins	tantaneous loop -:	> 500mS debour	ice		
; bit	num	bers	for flag	qs					
TFlag			EQU 7	; tim	er will need serv	ice			
AFlag			EQU 6	; an	alarm has occurred	d			
DFlag			EQU 5	; in	delayed (45 sec) a	alarm mode			
DOpen			EQU 4	; the	delay loop just d	opened			
- IOpen			EQU 3	; the	inst loop just op	pened			
; Stai	ndar	d Eq	uates						
W			EQU O						
F			EQU 1						
GPWUF			EQU 7						

PAO	EQU	5	
ТO	~ FOI	4	
10	EQ0	2	
PD	EQU	3	
Z	EQU	2	
Zero	EQU	2	
DC	FOU	1	
20 C	FOU	-	
	ЕQU	0	
Carry	EQU	0	
; Fuses			
MCLRDisable	d EOU	0	
MCLPEnabled	FOIL	u:10:	
A J P	EQU		
CodeProtect	EQU	0	
NoCodeProte	ct EQU	н'08'	
WDTDisabled	EQU	0	
WDTEnabled	EOU	н'04'	
IntRCOgc	F∩II	H:02:	
THEREOSE The DCO and	EQU	11 02	
EXTRCOSC	EQU	H'03'	
XTOsc	EQU	H'01'	
LPOsc	EQU	0	
· IEOR Bogi	ator Maai	mmonta	
, JUO REGI	DUCL ASSI		
TNDF.	EQÚ	н'UU'	
TMR0	EQU	н'01'	
PCL	EQU	Н'02'	
STATUS	EOU	H'03'	
ECD	EOU	11 0.0 1	
r SR	ЕQU	H 04	
OSCCAL	EQU	H'05'	
GPIO	EQU	Н'Об'	
; program v	ariables		
Flage	FOIT	¥'07' :	storage spage for messages
Tidg5			
THISSW	EQU	H'08' i	100p-switch status
; switch0 -	the Delay	yed Loop	
LastSw0	EOU	н'09';	last value - for detecting change
	200		
SwState0	FOIL	H:0A: :	the official (debounced) status
SwState0	EQU	H'OA' ;	the official (debounced) status
SwState0 DBTimer0	EQU EQU	H'OA' ; H'OB' ;	the official (debounced) status counter used during debouncing of the switch
SwState0 DBTimer0	EQU EQU	H'OA' ; H'OB' ;	the official (debounced) status counter used during debouncing of the switch
SwState0 DBTimer0 ; switch1 -	EQU EQU the Insta	H'OA' ; H'OB' ; ant Loop	the official (debounced) status counter used during debouncing of the switch
SwState0 DBTimer0 ; switch1 - LastSw1	EQU EQU the Insta EQU	H'0A' ; H'0B' ; ant Loop H'0C' ;	the official (debounced) status counter used during debouncing of the switch
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1	EQU EQU the Insta EQU EQU	H'0A' ; H'0B' ; ant Loop H'0C' ; H'0D' ;	the official (debounced) status counter used during debouncing of the switch
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DDTimer1	EQU EQU the Insta EQU EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ;	the official (debounced) status counter used during debouncing of the switch
SwState0 DBTimer0 ; switchl - LastSwl SwState1 DBTimer1	EQU EQU the Insta EQU EQU EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ;	<pre>the official (debounced) status counter used during debouncing of the switch "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1	EQU EQU the Insta EQU EQU EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ;	<pre>the official (debounced) status counter used during debouncing of the switch "" "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 -	EQU EQU the Insta EQU EQU EQU the Panio	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; c Switches	the official (debounced) status counter used during debouncing of the switch "" "" ""
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2	EQU EQU the Insta EQU EQU EQU the Panic EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ "" "" "" "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2	EQU EQU the Insta EQU EQU the Panic EQU EOU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ;	<pre>the official (debounced) status counter used during debouncing of the switch "" "" "" "" "" "" "" "" "" "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2	the Insta EQU EQU EQU EQU EQU the Panic EQU EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ;	<pre>the official (debounced) status counter used during debouncing of the switch "" "" "" "" "" "" "" "" "" "" "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2	the Insta EQU EQU EQU EQU EQU the Panic EQU EQU EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ;	<pre>the official (debounced) status counter used during debouncing of the switch "" "" "" "" "" "" "" "" "" "" "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2	EQU EQU the Insta EQU EQU EQU the Panic EQU EQU EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ "" "" "" "" "" "" "" "" "" "" "" "</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer	EQU EQU the Insta EQU EQU EQU the Panic EQU EQU EQU bytes	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ; H'11' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ "" "" "" "" "" "" "" "" "" "" "" "</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo	EQU EQU the Insta EQU EQU EQU the Panic EQU EQU EQU bytes EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ; H'11' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ """ """ Lo byte of the timer</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid	EQU EQU the Insta EQU EQU the Panic EQU EQU EQU bytes EQU EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ; H'11' ; H'12' ; H'13' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ "" "" "" "" Lo byte of the timer Mid ""</pre>
<pre>SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi</pre>	EQU EQU the Insta EQU EQU EQU EQU EQU EQU EQU EQU EQU EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ; H'12' ; H'13' ; H'14' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ "" "" "" Lo byte of the timer Mid "" High ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi	EQU EQU EQU the Insta EQU EQU EQU EQU EQU EQU EQU EQU EQU EQU	H'0A' ; H'0B' ; ant Loop H'0C' ; H'0D' ; H'0E' ; H'0E' ; H'1C' ; H'11' ; H'12' ; H'13' ; H'14' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ """ """ """ """ """ """ "</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi	the Insta EQU EQU the Insta EQU EQU EQU EQU EQU EQU EQU EQU EQU	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ; H'12' ; H'13' ; H'14' ;	<pre>the official (debounced) status counter used during debouncing of the switch "" "" "" "" "" "" "" "" "" "" "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi ; The ID	EQU EQU EQU the Insta EQU EQU EQU EQU EQU EQU EQU EQU EQU Words	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ; H'12' ; H'12' ; H'13' ; H'14' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ """ "" "" "" "" "" "" "" "</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerL0 TimerMid TimerHi ; The ID	EQU EQU EQU the Insta EQU EQU EQU the Panic EQU EQU EQU EQU EQU EQU Words	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ; H'11' ; H'12' ; H'12' ; H'13' ; H'14' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ """ """ """ """ """ """ "</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerL0 TimerMid TimerHi ; The ID	EQU EQU EQU the Insta EQU EQU EQU the Panic EQU EQU EQU EQU EQU EQU Words ORG H'020	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ; H'11' ; H'12' ; H'12' ; H'13' ; H'14' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ Lo byte of the timer Mid "" High ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerL0 TimerMid TimerHi ; The ID ID0	EQU EQU EQU the Insta EQU EQU EQU EQU bytes EQU EQU Words ORG H'020 Data.W	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ; H'12' ; H'13' ; H'14' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ "" "" "" Lo byte of the timer Mid "" High ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi ; The ID ID0 ID1	EQU EQU EQU the Insta EQU EQU EQU EQU EQU EQU Words ORG H'020 Data.W Data.W	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ; H'12' ; H'13' ; H'13' ; H'14' ;	<pre>the official (debounced) status counter used during debouncing of the switch "" "" "" "" "" "" "" "" "" "" "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi ; The ID ID0 ID1	EQU EQU EQU the Insta EQU EQU EQU EQU EQU EQU EQU EQU Words ORG H'020 Data.W Data.W	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ; H'12' ; H'13' ; H'14' ;	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ """ """ "" "" "" "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi ; The ID ID0 ID1 ID2	EQU EQU EQU the Insta EQU EQU EQU the Panic EQU EQU EQU EQU Words ORG H'020 Data.W Data.W	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ; H'11' ; H'12' ; H'13' ; H'14' ; O' H'0000' H'0000' H'0003'	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ """ "" Lo byte of the timer Mid "" High ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi ; The ID ID0 ID1 ID2 ID3	EQU EQU EQU the Insta EQU EQU EQU the Panic EQU EQU EQU bytes EQU EQU Words ORG H'020 Data.W Data.W Data.W	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ; H'11' ; H'12' ; H'13' ; H'14' ; O' H'0000' H'0000' H'0003' H'000F'	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ Lo byte of the timer Mid "" High ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerL0 TimerMid TimerHi ; The ID ID0 ID1 ID2 ID3	EQU EQU EQU the Insta EQU EQU EQU the Panic EQU EQU bytes EQU EQU Words ORG H'020 Data.W Data.W Data.W	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ; H'12' ; H'12' ; H'13' ; H'14' ; O' H'0000' H'0000' H'0000' H'0000' H'0005'	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ Lo byte of the timer Mid "" High ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi ; The ID ID0 ID1 ID2 ID3 ; and th	EQU EQU EQU the Insta EQU EQU EQU EQU bytes EQU EQU words ORG H'020 Data.W Data.W Data.W	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ; H'11' ; H'12' ; H'12' ; H'13' ; H'14' ; O' H'0000' H'0000' H'0000' H'0005' CS	<pre>the official (debounced) status counter used during debouncing of the switch """ """ """ """ """ """ """ """ """ "</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi ; The ID ID0 ID1 ID2 ID3 ; and th	EQU EQU EQU the Insta EQU EQU EQU EQU bytes EQU EQU words ORG H'020 Data.W Data.W Data.W	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; C Switches H'OF' ; H'10' ; H'11' ; H'12' ; H'13' ; H'13' ; H'14' ; O' H'0000' H'0000' H'0000F' ES	<pre>the official (debounced) status counter used during debouncing of the switch "" "" "" "" "" "" "" "" "" "" "" "" ""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi ; The ID ID0 ID1 ID2 ID3 ; and th	EQU EQU EQU the Insta EQU EQU EQU EQU bytes EQU EQU words ORG H'020 Data.W Data.W Data.W e Fuse bit ORG	H'OA' ; H'OB' ; ant Loop H'OC' ; H'OD' ; H'OE' ; H'OE' ; H'OF' ; H'10' ; H'11' ; H'12' ; H'12' ; H'13' ; H'13' ; H'14' ; O' H'0000' H'0000' H'0000F' ES H'OFFF'	<pre>the official (debounced) status counter used during debouncing of the switch """"""""""""""""""""""""""""""""""""</pre>
SwState0 DBTimer0 ; switch1 - LastSw1 SwState1 DBTimer1 ; switch2 - LastSw2 SwState2 DBTimer2 ; The timer TimerLo TimerMid TimerHi ; The ID ID0 ID1 ID2 ID3 ; and th CONFIG	EQU EQU EQU the Insta EQU EQU EQU EQU bytes EQU EQU EQU words ORG H'020 Data.W Data.W Data.W e Fuse bit	H'0A' ; H'0B' ; ant Loop H'0C' ; H'0D' ; H'0E' ; C Switches H'0F' ; H'10' ; H'10' ; H'12' ; H'12' ; H'13' ; H'14' ; O' H'0000' H'0000' H'0000' H'0000F' ES	<pre>the official (debounced) status counter used during debouncing of the switch """"""""""""""""""""""""""""""""""""</pre>

```
*****
          Power on jumps to here ...
     *****
       ORG
               H'00'
       MOVWF
               OSCCAL
                           ; store the factory osc. calibration value
       GOTO
               Init
                           ; and jump past the subroutines
    *****
;
      The Subroutines...
;
    *******
;
      DoTime
;
    Updates all of the timers, and controls the LED output
;
    Returns with W = 0 if there was no time change, and W = 1 if there was.
;
    Must be called at least once every msec to catch Timer0 overflows.
;
DoTime BTESS
               TMR0.7
                                ; high bit of timer set?
      GOTO
               dt1
                                ; no - we've overflowed
       BSF
               Flags, TFlag
                                ; yes, set the flag
      RETLW
               0
dt1
      BTESS
               Flags, TFlag
                                ; have the timers been serviced?
      RETLW
               0
                                ; yes - that's all
    when Timer0 overflows, all timers are serviced...
;
       INCE
              DBTimer0,F
                              ; increment the debounce timers
       INCE
               DBTimer1.F
       INCF
               DBTimer2,F
       INCE
               TimerLo,F
                           ; and the delay timers
       BTFSC
               STATUS, Zero
               TimerMid,F
       INCF
       BTFSC
               STATUS, Zero
       INCF
               TimerHi,F
       BCF
               Flags, TFlag
                                ; reset the timer service flag,
       CLRWDT
                                ; and the WatchDog timer
      BTFSC
                                ; has there been an alarm?
               Flags,AFlag
       GOTO
               SlowFlash
                           ; if so, flash the LED
       BTFSS
               Flags,DFlag
                                ; are we in delayed entry/exit?
      RETLW
               1
                                ; no, just return
FastFlash
               TimerLo,5
                           ; for the fast flash rate
      BTFSS
       BCF
               GPIO,LED
                           ; match LED drive to the 64ms bit
      BTFSC
               TimerLo,5
               GPIO,LED
      BSF
      RETLW
               1
SlowFlash
      MOVF
               TimerMid,W
                               ; for slow rate, use 0.5 seccounter
               B'00000011'
                                ; but only the low two bits of it
      ANDLW
      BTESS
               STATUS, Zero
                                ; to drive the LED
      BCF
               GPIO,LED
       BTFSC
               STATUS, Zero
      BSF
               GPIO,LED
      RETLW
               1
    ******
;
      DelayChk
;
    Gets the current (debounced) status of the loop connected to GPO (pin7)
;
    Returns with W=1 if there's a change in state, W=0 otherwise, and
;
    sets the DOpen bit of Flags set when the loop opens.
;
DelayChk
```

; assume loop-switch is not open CLRF ThisSw BTFSC GPIO, DelayIn ; check sw status INCE ThisSw,F ; the loop is open Compare to last state ; MOVF LastSw0,W XORWF ThisSw,W BTFSS STATUS,Zero ; Zero is set if there's been no change GOTO dc1 No change since last scan, but are we in a debounce phase? ; SwState0,W MOVF XORWF ThisSw,W ; compare present state to the official state STATUS,Zero ; Zero is Clr if they differ(we're in debounce) BTESC RETLW 0 ; Input is changing, have we gone past the debounce time? DBDelay ; get the debounce time DBTimer0,W ; and compare to elapsed MOVLW SUBWE BTFSS STATUS, Carry ; Carry is set if DBTimer0 >= DBDelay RETLW 0 ; we've exceeded the debounce time - change the official state of theloop MOVF ThisSw.W MOVWF SwState0 ; store current state, and ; check if it's zero (loopnormal) BTFSS STATUS,Zero BSF Flags,DOpen ; if not, set the DOpen bit RETLW 1 ; the input is changing state - prepare to debounce dc1 ThisSw,W MOVF MOVWF LastSw0 ; remember this passes' state CLRF DBTimer0 ; and reset the debounce timer RETLW 0 ****** ; ; InstChk Gets the current (debounced) status of the loop connected to GP1 (pin6) ; Returns with W=1 if there's a change in state, W=0 otherwise, and ; sets the IOpen bit of Flags set when the loop opens. ; InstChk CLRF ThisSw ; assume loop-switch is not open GPIO,InstIn BTFSC ; check sw status INCF ThisSw,F ; the sw was pressed Compare to last state ; MOVF LastSw1.W XORWF ThisSw,W BTFSS STATUS,Zero ; Zero is set if there's been no change GOTO ic1 No change since last scan, but are we in a debounce phase? ; MOVF SwState1,W XORWF ThisSw,W ; compare present state to the official state ; Zero is Clr if they differ (we're in debounce) BTESC STATUS,Zero RETLW 0 ; Input is changing, have we gone past the debounce time? MOVLW DBInst ; get the debounce time DBTimer1,W ; and compare to elapsed SUBWE BTFSS STATUS, Carry ; Carry is set if DBTimer1 >= DBInst RETLW ; we've exceeded the debounce time - change the official state of the loop ThisSw.W MOVF MOVWF SwState1 ; store current state and

```
STATUS, Zero
                                 ; check if it's zero (loopnormal)
       BTFSS
       BSF
                                 ; if not, set the IOpen bit
               Flags, IOpen
       RETLW
               1
;
    the input is changing state - prepare to debounce
ic1
               ThisSw,W
       MOVF
       MOVWF
               LastSw1
                                 ; remember this passes' state
                        ; and reset the debounce timer
       CLRF
               DBTimer1
       RETIW
               0
    ******
;
       PanicChk
;
    Gets the current (debounced) status of the switches connected to GP3(pin 4)
;
    Internally stored as SwState2=1 if a panic switch is pressed (input =OV),
;
    and SwState2=0 if no switch is pressed
;
    Always returns with W = 0, and Zero bit of Carry set for no switch pressed
;
PanicChk
       CLRF
               ThisSw
                                ; assume switch is not pressed
       BTFSS GPIO, PanicIn ; check sw status
       INCF
               ThisSw,F
                          ; the sw was pressed
    Compare to last state
;
       MOVF
               LastSw2.W
       XORWE
               ThisSw,W
       BTFSS
               STATUS,Zero
                                ; Zero is set if there's been no change
       GOTO
               pc1
    No change since last scan, but are we in a debounce phase?
;
       MOVF
               SwState2,W
       XORWF
               ThisSw,W
                            ; compare present state of sw to the official state
                              ; Zero is Clr if they differ (we're in debounce)
       BTESC
               STATUS,Zero
       GOTO
               pc2
    Switch is changing, have we gone past the debounce time?
;
       MOVLW
               DBPanic ; get the debounce time
DBTimer2,W ; and compare to elapsed
       SUBWE
       BTFSS
               STATUS, Carry ; Carry is set if DBTimer2 >= DBPanic
       GOTO
               pc2
    we've exceeded the debounce time - change the official state of the switch
;
             ThisSw.W
       MOVF
       MOVWF
               SwState2
                           ; store current state in SwState
       GOTO
               pc2
    the switch input is changing state - prepare to debounce
;
pc1
      MOVE
               ThisSw.W
               LastSw2
                                 ; remember this passes' state
       MOVWF
               DBTimer2
                          ; and reset the debounce timer
       CLRF
;
    we're done for this pass...
    MOVF
pc2
               SwState2,F
                                 ; check the switch state,
      RETIW
               0
                                 ; and return
    ;
    *****
;
;
;
    Here's where it all begins...
    *****
; Set up the Option Register and the IO port...
Init MOVLW
               B'10000010' ; use intclock input, /8 prescaler
       OPTION
                                ; pullups on, and no wakeup on change
                                ; output all 0s on a powerup
; GP0, GP1 and GP3 are inputs,
       CLRF
               GPIO
               B'00001011'
       MOVLW
                          ; GP2, GP4 and GP5 are outputs
               GPIO
       TRIS
```

; Clear the variable RAM... MOVLW H'1F' ; point to the last RAM location MOVWF FSR cr1 CLRF INDF ; and zero it DECF FSR,F ; loop until FSR points to Register6, MOVF FSR,W B'11100110' XORLW ; allowing that FSR<7:5> are always 1s STATUS, Zero BTFSS GOTO cr1 ; For the first 45 secs, only the panic switch input is active... Flags,DFlag ; fast flash the LED BSF wait1 CALL DoTime CALL PanicChk ; check for a panic switch STATUS,Zero BTFSS GOTO Alarm D'86' ; check for timeout MOVLW SUBWE TimerMid,W ; 86 counts is ~ 45secs BTFSS STATUS,Carry GOTO wait1 ; keep looping until time is up BCF Flags,DFlag ; stop the flashing LED BSF GPIO,LED ; and make sure it's off ; Now all three loops are active, watch them forever... Main CALL DoTime ; keep clock 'running' CALL PanicChk ; see if a panic switch is pressed ; skip on if it isn't BTFSS STATUS,Zero GOTO Alarm ; and alarm if it is ; check the Inst loop InstChk CALL BTFSC Flags,IOpen ; and alarm on opening GOTO Alarm DelayChk CALL ; check the delayed loop Flags, DOpen ; and alarm on opening BTFSS GOTO Main ; else, just loop ; Waiting 45 Seconds before alarming... DelAlarm CLRF TimerLo ; reset the timer CLRF TimerMid ; (saves doing arithmetic) CLRF TimerHi BSF Flags,DFlag ; fast flash the LED da1 CALL DoTime PanicChk ; check for a panic switch CALL BTFSS STATUS,Zero GOTO Alarm CALL InstChk ; or a break in the Inst loop BTFSC Flags,IOpen ; and alarm on opening GOTO Alarm MOVLW D'86' ; check for timeout ; 86 counts is ~ 45 secs SUBWF TimerMid,W BTFSS STATUS,Carry GOTO da1 ; keep looping until time is up ; It's an alarm, turn the outputs on... (for 5 mins max) Alarm BSF Flags, AFlag ; there's been an alarm, flagit TimerLo CLRF ; reset the timer CLRF TimerMid CLRF TimerHi BSF GPI0,Const ; turn the continuous output on

all	CALL	DoTime		
	CALL	PanicChk	;	still have to keep current
	CALL	InstChk		; on the status of the inputs
	CALL	DelayChk		
	MOVF	TimerMid,W		
	ANDLW	B'00000011'		
	BTFSC	STATUS,Zero		; set the pulsed output based on the
	BCF	GPIO,Pulse		; low two bits of the 0.5 sec timer
	BTFSS	STATUS,Zero		
	BSF	GPIO,Pulse		
	MOVLW	D'02'	;	check for >5 mins
	SUBWF	TimerHi,W	;	~2*134sec + 60*0.5sec
	BTFSS	STATUS,Carry		
	GOTO	al1		; keep looping until timed out
	MOVLW	D'60'		
	SUBWF	TimerMid,W		
	BTFSS	STATUS,Carry		
	GOTO	all		
	BCF	GPIO,Const		; >5 mins - stop the alarms
	BCF	GPIO,Pulse		
	BCF	Flags,DOpen		; reset the change of state bits
	BCF	Flags,IOpen		
	GOTO	Main	;	and start all over

END

NOTES: