

NETSWITCH

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APPLICATION OPERATION

This gadget is designed for individually dimming lamps, turning on/off electrical devices, etc. It accepts commands via serial lines individually, (i.e., more than one NETSWITCH can connect to same serial line, or you can use its switches to control it).

You can handle up to 256 turning points with this device, and you can control them from one central station. (The central station description is not included in this text). There are two modes on device: Manual or Serial.

NODE-ADDRESSBYTE-COMMAND-COMMANDPARAM-ENDBL

Switching between modes is only allowed from serial commands. It starts Manual Mode when powered up.

Manual Mode

There are three buttons on the device. One for turning on/off the load, the other two are push buttons which let you dim the load. When the on/off switch is turned on, dimming is not allowed. When turned off, dimming is allowed and you have to dim to zero intensity if you want to turn off.

Serial Mode

The device is able to receive (only) data from the serial line at 1200 bps. Being connected to the same serial line and to more than one NETSWITCH is allowed, because every NETSWITCH has a unique number to identify. (This number not changeable, it is set compile time, though, this is not benefit.)

The NETSWITCH accepts blocks of bytes only as follows:

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Where:

- NODE: NODE command.
- ADDRESSBYTE: This is a unique number.
- COMMAND: One of commands below.
- COMMANDPARAM: Optional, it depends from command.
- ENDBL: This is a terminator command.

The acceptable commands are:

- NODE: This command means the start of block, its value is ASCII 'N'.
- MANU: This command puts the NETSWITCH to manual mode. When in this mode, it accepts its switches. Its value is ASCII 'M'.
- SERI: This command puts the NETSWITCH to serial mode. When in this mode, it accepts serial commands only. Its value is 'S'.
- SWOFF: This command turns off the NETSWITCH. Its value is ASCII 'F'.
- SWON: This command turns on the NETSWITCH. Its value is ASCII 'O'.
- DIMM: This command dimms the load, according to its parameter. Its value is ASCII 'D'.
- ENDBL: This is END of BLock command, every block has to end with it. Its value is ASCII 'E'.

BLOCK DIAGRAM



MICROCHIP HARDWARE DEVELOPMENT TOOLS USED:

Assembler/Compiler Version:

MPASM v1.40

APPLICATION OPERATION

Hardware Description

It consists of three main parts:

- 1. The power supply. The gadget obtains it from another 5V source now (e.g., it comes with serial line).
- The PIC12C508. It is controlled in two ways.
 a)In manual mode with push buttons and switch.
 b)In serial mode with serial commands.

The PIC12C508 runs with its on-chip (internal) RC oscillator, nominally 4 MHz. The PIC12C508 controls the power switching unit by a PWM signal. It is available on pin 7 of PIC12C508.

- 3. The power switching unit. This unit switches (or dimms) the load. For a half period:
 - a)When the period starts, the zener stabilizes voltage at 10V.
 - b)The opto-coupler receiving side transistor, NPN, and PNP transistor are voltage controlled current source. This current is available on collector of PNP. It charges a capacitor. When this capacitor reaches threshold of the UJT (uni-junction transistor), it discharges the cap and produces an ignition pulse for thyristor (SCR). You can choose a right thyristor for your requirements.

Software Description

The software contains a loop which gets the state of input pins, then evaluates serial communication, the commands received via the serial line, the state of buttons and then does the appropriate action, i.e. controls SCRs with a PWM sign.

There is a delay subroutine in the loop, which controls the time usage. When the device is not receiving a serial sign, the delay routine does a half time cycle (half of one bit time of 1200 bps). When the device is receiving, it does a complete time cycle (one bit time of 1200 bps). This routine is based on the TMR0. (TMR0 is in timer mode, counts internal clock and has a prescaler 1:4). For additional information, see the file netsw.asm, it has many comments.



GRAPHICAL HARDWARE REPRESENTATION



APPENDIX A: SOFTWARE LISTING

```
title "NETSW.ASM V1.0 "
; Electromechanical switch replacement.
; This program controls the NETSWITCH.
                    list p=12c508
#include "p12c508.inc"
; The configuration bits are configured as follows:
; MCLRE-disabled (0)
; CP-disabled (1)
; WDTE-enabled (1)
; OSC-internal RC (10)
; The next definitions are the commands that accepted by NETSWITCH.
                                              ; NODE address, ASCII "N"
#define NODE 0x4E
                                              ; MANUal mode, ASCII "M"
#define MANU 0x4D
#define SERI 0x53
                                              ; SERIal mode, ASCII "S"
#define SWOFF 0x46
                                              ; SWitch turn OFF, ASCII "F"
#define SWON 0x4F
                                              ; SWitch turn ON, ASCII "O"
#define DIMM 0x44
                                              ; DIMMing, ASCII "D"
#define ENDBL 0x45
                                              ; END of BLock, ASCII "E"
; Each block that controls the NETSWITCH has to meet the next form:
; NODE-ADDRESSBYTE-COMMAND-COMMANDPARAM-ENDBL
; NODE - Command NODE
; ADDRESSBYTE - This is the NODE address
; COMMAND - COMMAND byte, see above (SERI, MANU, SWOFF, SWON, DIMM)
; COMMANDPARAM - One byte, for DIMM command only
                                    (e.g. DIMM 0x7F means half intensity)
; ENDBL - END of BLock
NETSWOPTION
                    equ 0xCl
                                  ; Bin: 11000001, Tim. presc.to TMR0, 1:4
NULL
                    equ 0x00
EIGHT
                    equ 0x08
                                   ; Only Bit 0 is output
GPSET
                    equ 0xFE
BLSIZE
                    equ 0x05
                                   ; BLockSIZE, it is 5 now. See above.
MYADDR
                    equ 0x10
                                   ; <- This is an example
                                  ; MINimal PWM value, i.e. totally turn off
                   equ 0x00
MINPWM
                   equ 0x01
                                  ; MAXimal PWM value
MAXPWM
BUTTONCYCLE
                   equ 0x7F
                                  ; For button delaying
HALFTIME
                    equ 0x92
                                  ; This is a half cycle value. (416 usec)
                   equ 0x08
ReadBuf
                                  ; Holds input info
SWStatus
                   equ 0x09
                                  ; Holds all system status
NumBits
                    equ 0x0A
                                   ; Number of bits
                    equ 0x0B
Counter
EvalComBuf
                                  ; Temporary byte for holding changes
                   equ 0x0C
PWMCycleCount
                                  ; For PWM signal
                   equ 0x0D
PWMPulseCount
                  equ 0x0E
                                  ; For PWM signal
                    equ 0x0F
                                  ; Receive Buffer
RecBuf
DIMMParam
                    equ 0x10
                                  ; DIMMing Parameter
                    equ 0x11
                                  ; Button delaying
ButtonCounter
BLTABLE
                    equ 0x14
                                   ; BLock TABLE starts here
; The next bits are used in SWStatus byte
                    equ 0x00
RecInPr
NewByte
                    equ 0x01
BLInPr
                    equ 0x02
ManMode
                    equ 0x03
Pulse
                    equ 0x04
```

NewBL PWMUpdate	equ 0x05 equ 0x06	
; The next bits are	used in GPIO	
_		
Rx	equ 0x05	
UpButt	equ 0x02	
DownButt	equ 0x03	
SW	equ 0x04	
PWMOut	equ 0x00	
MSB	equ 0x07	
	org 0x00	
Start	call Init	: Call Init procedure
MainLoop	call CetState	: Cet all input state
Матпьоор	call Evolopial	· Evoluate geniel input
		, Evaluate Serial Input
	call RecBlock	, Receive all byte in block
	call EvalCommand	; Evaluate command via serial input
	call EvalButtons	; Evaluate pushbuttons and switch
	call DoOut	; Do appropriate action
	call Delay	; Delay cycle
	clrwdt	; Clear watchdog
	goto MainLoop	; Do again
Init	movlw NETSWOPTION	i
	option	;
	movlw EIGHT+1	;
	movwf FSR	;
	movlw 0x0D	;
	movwf 0x08	;
DoAgainClear	decfsz 0x08,F	;
	goto ClearOneRAM	;
Continue	movlw EIGHT	;
	movwf NumBits	;
	movlw GPSET	;
	tris GPIO	;
	movlw HALFTIME	;
	movwf TMR0	;
	retlw NULL	;
ClearOneRAM	clrf INDF	;
	incf FSR.F	;
	goto DoAgainClear	;
GetState	movf GPIO.W	; Read pins
deebeate	mover BradBuf	: Dut to ReadBuf
	retlw NULL	; And return
FuelSerial	htfag SWStatus BogInDr	: PECeiving IN prograds bit tost
EVALOCITAL	acto NevtBit	. RECEIVING IN PROGRESS DIL LESL
	btfgg DoodDuf Dr	, BY bit toot start condition true 2
	belss Readbul, RX	, RA DIT LEST, Start Condition true ?
NoutDit	DSI SWStatus, Recimpr	, Yes, set RECeiving in Progress bit
	LECIM NULL	· NV, IELUIII
NextBlt	DIISC ReadBul, RX	RADIT TEST
	DSI RECBUI,MSB	; II KX SET, SET MSB OI RECBUI
	DCI STATUS,C	Clear Carry
	rri RecBui,F	; Rotate Right
	dectsz NumBits,F	; Is there more bits ?
	retiw NULL	; Yes, return
	bcf SWStatus,RecInPr	; No, clear status bit
	bsf SWStatus,NewByte	; This is new byte
	movlw EIGHT	; Prepare next serial receiving
	movwf NumBits	i
	retlw NULL	; Return

; This routine called RecBlock waits until all element of the command

; block is received by setting ${\tt BLInPr}$ (BLock receiving In <code>PRogress</code>) bit in

; SWStatus. It writ	e the received b	pytes to BLTable.	
RecBlock NextByte	btfss SWStatus movf RecBuf,W	,NewByte retlw NULL bcf SWStatus,NewByte btfsc SWStatus,BLInPr goto NextByte bsf SWStatus,BLInPr movlw BLTABLE movwf FSR movlw BLSIZE movwf Counter movwf INDF	<pre>; Is there new byte ? ; No, return ; Clear new byte status ; Is receiving in progress ? ; Yes, go to NextByte ; No, sets BLInPr bit ; BLTABLE to ; FSR ; BLSIZE to ; Counter ; Put contents to ; (FSR), i.e. to BLTable</pre>
· The routine galle	d FuelCommand or	incf INDF,F decfsz Counter,F retlw NULL bcf SWStatus,BLInPr bsf SWStatus,NewBL retlw NULL	<pre>; Points to next element ; Is there more byte ? ; Yes, return ; No, clears BLInPr bit ; This is new block ; Return, the BLTable is ready to use</pre>
/ The Toucine Carle		aidates the bliable.	
EvalCommand	btfss SWStatus	<pre>,NewBL ; Is there : retlw NULL btfsc SWStatus,BLInPr; retlw NULL movlw BLTABLE; No, BLT movwf FSR movlw NODE call EvalCell btfss STATUS,Z goto ClearTable movlw MYADDR call EvalCell btfss STATUS,Z goto ClearTable movlw MANU call EvalCell btfss STATUS,Z goto ChesERI bsf EvalComBuf,ManMode goto ChkEND</pre>	<pre>new block ? ; No Is block receiving in progress ? ; Yes, return ABLE to ; FSR ; This must be the first command ; Is the command rigth ? ; ; No, go to ClearTable ; Yes, ; Check, NODE ADDRESS is matching ? ; ; No, go to ClearTable ; Yes, ; Is the next command MANU ? ; ; ; No, go to ChkSERI ; Yes, sets ManMode ; Go to ChkEND</pre>
ChkSERI	movlw SERI	; call EvalCell btfss STATUS,Z goto ChkSWON bcf EvalComBuf,ManMode goto ChkEND	; Is the command SERI ? ; ; ; Yes, clears ManMode ;
ChkSWON	movlw SWON	; call EvalCell btfss STATUS,Z goto ChkSWOFF; movlw MAXPWM; movwf DIMMParam; DIMMP goto ChkEND	; Is the command SWON ? ; aram sets to MAXPWM ;
ChkSWOFF	movlw SWOFF	; call EvalCell btfss STATUS,Z goto ChkDIMM movlw MINPWM movwf DIMMParam; DIMMP goto ChkEND	; Is the command SWOFF ? ; ; ; aram sets to MinPWM ;
ChkDIMM	movlw DIMM	; call EvalCell btfss STATUS,Z	; Is the command DIMM ? ;

		goto ChkEND	;
ChkParam	movf INDF,W		; Yes, the Parambyte is
		movwf DIMMParam	; transferred to DIMMParam :
ChlEND		moulw ENDRI	· In the command ENDRI 2
CIIKEND		all EvalColl	· IS CHE COMMAND ENDEL :
		btfee STATUS 7	:
		goto CloarTable	· No co to CloarTable
		goto cleariable	, NO, GO LO CLEARIADIE
		MOVI EVAICOMBUL,W	, Yes, all changes are written to
Q] a a sema b] a		IOTWI SWStatus,F	, SWSLALUS
Clearlable	MOVIW BLIABLE		, BLIABLE LO
		MOVWL FSR	, FSR
		MOVIW BLSIZE	, BLSIZE LO
	1 6 79757	movwi Counter	; Counter
ClearCycle	CITI INDE	; Clears one	e element
		inci FSR,F	; Points to next element
		decisz Counter, F; is th	nere more byte ?
		goto ClearCycle	; Yes,
		movlw NULL	; No, all changes are discarded
		movwi DIMMParam;	
		bcf_SWStatus,NewBL;	
		retlw NULL	; Return
EvalCell		subwf INDF,W	; W and INDF are equal ? (affects Z)
		btfss STATUS,Z	;
		retlw NULL	; No, (Z remains)
		incf FSR,F	; Yes, points next element
		movlw NULL	; Sets Z, since NULL = 0
		retlw NULL	; return (Z remains)
; mode (MANU bit set) of course.		
EvalButtons	btfss SWStatus	,ManMode; Are buttons er	nabled ?
		retlw NULL	; No
		btfss ReadBuf,SW	; Yes, Does SWitch turn on ?
		goto ChkUpButt	; No, go to check pushbuttons
SWTurnOn	movlw MAXPWM	; Yes, turn	on
		movwf DIMMParam;	
		retlw NULL	;
ChkUpButt	btiss SWStatus	,UpButt	;
		goto ChkDownButt	;
		decisz ButtonCounter,F	;
		retiw NULL	<i>i</i>
		MOVIW MAXPWM	;
		Subwi DIMMParam,w	;
		btiss STATUS,Z	;
	btfra OMOtotur	deci DIMMParam,F	i .
CHRDOWHBULL	DLISS SWSLatus	, DownBull	
		golo Retrombull	·
		metly NULL	
		TECIW NOLL	
		MOVIW MINPWM	
		btfag CTATUS Z	
		buiss Status,2	
		Inci DIMMParam,F	;
RetFromButt	MOATM BOLLONGI		i .
		movwi BullonCounter	,
		retiw NULL	i
· mba wawting sallad	Defect malage	DUM sign desceding on D	
; The routine called ; If DIMMParam is 0	no PWM gign ev	ist the output pip is	Immralam. HIGH
, II DIMMFALAN IS U,	10 FWM SIGH EX	ist, the output pin is .	
DoOut	btfss SWStatus	,PWMUpdate ; Does PWM r	need to update ?
		retlw NULL	; No
		decfsz PWMCvcleCount.F	; Yes, Decrement PWM cycle
		2	

		goto PWMPulse	;
		bsf GPIO,PWMOut	; Prepare
		movf DIMMParam,W	; next
		movwf PWMPulseCount	; pulse
		bsf SWStatus, Pulse;	
		retlw NULL	;
PWMPulse	btfss SWStatus,	Pulse ; Need a pulse	cycle ?
		retlw NULL	; No
		decfsz PWMPulseCount,F	; Yes, do
		retlw NULL	;
		bcf GPIO,PWMOut; clear out	put and
		bcf SWStatus,Pulse	; status
		retlw NULL	;
Delay		btfss SWStatus,RecInPr	; Is receiving in progress ?
		goto HalfCycle	; No, polling rate is about 416 us
		call HalfCycle;Yes, poll.	rate is 833 us first half
		movlw 0x10 ;	
		addwf TMR0,F;	
		call HalfCycle	; Second half
		bsf SWStatus,PWMUpdate;	
		retlw NULL	;
HalfCycle	movf TMR0,W		;
		btfss STATUS,Z	; Need to wait ?
		goto HalfCycle	; Yes
		movlw HALFTIME	; No
		movwf TMR0 ; Reload	
		<pre>btfss SWStatus,PWMUpdate;</pre>	
		goto SetPWMUpdate;	
ClearPWMUpdate	bcf SWStatus,PW	MUpdate	;
		retlw NULL	;
SetPWMUpdate	bsf SWStatus,PW	MUpdate	;
		retlw NULL	i
		end	;

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